Problem Spaces P/NP

P/NP

- Chapter 7 of the book
- We'll skip around a little bit and pull in some simpler, alternate "proofs"
- Intractable Problems
 - Refer to problems we cannot solve in a reasonable time on the Turing Machine/Computer
 - Dividing line is exponential vs. polynomial, even a big polynomial; e.g. O(n¹⁰⁰⁰⁰)

Class of Languages P

- If a deterministic Turing Machine M has some polynomial p(n) such that M never makes more than p(n) moves when presented with input of length n, then M is said to be a polynomial time TM
- P is the set of languages accepted by polynomial time Turing Machines



Class of Languages NP

- If a non-deterministic Turing Machine N has some polynomial p(n) such that N never makes more than p(n) moves in any sequence of choices when presented with input of length n, then N is said to be a polynomial time non-deterministic TM
 - These p(n) moves are for one "thread"; N forks off threads in parallel
- NP is the set of languages accepted by polynomial time non-deterministic Turing Machines











NP Complete Problems

- Most people believe that P ≠ NP due to the existence of problems in NP that are in the class NPC, or NP Complete
- NP Complete
 - The "hardest" problems of the class NP
 - Before we continue to define NPC problems, let's revisit the notion of reducibility







Some NP Complete Problems

- SAT, TSP, and CLIQUE are all NPC
- Here are some others:
 - Graph Coloring
 - Bin Packing
 - Knapsack
 - Subset Sum
 - 3SAT
 - Minesweeper Constraints
 - Many More

Graph / Map Coloring

- Given a graph with edges and nodes, assign a color to each node so that no neighboring node has the same color. Generally we want the minimum number of colors necessary to color the map (the chromatic number).
- Map coloring example: Nodes could be states on a map, color each state so no neighboring state has the same color and therefore becomes distinguishable from its neighbor



Can you determine the minimum number of colors for this graph?



Only known solution guaranteed to be optimal requires exponential time – examining all possible assignments, typically using backtracking after assigning colors



Bin Packing

- Suppose we have an unlimited number of bins each of capacity 1, and n objects of sizes s1, s2 ... s_n, where each s_i is a number between 0 and 1.
- Optimization: Determine the smallest number of bins into which the objects can be packed (and find the optimal packing)
- Decision: Given, in addition to the inputs described, an integer k, do the objects fit in k bins?
- Lots of applications; packing data in computer files/memory, filling orders from a product, loading trucks



Subset Sum

- This is a simpler version of the knapsack problem. The input is a positive integer C and n objects whose sizes are positive integers s1, s2, ... s_n.
- Optimization Problem: Among subsets of the objects with a sum at most C, what is the largest subset sum?
- Decision Problem: Is there a subset of the objects whose sizes add up to exactly C? e.g. electoral college problem



Problem a Problem is NP Complete

If we have a single problem P-NPC known to be NP-Complete, then:

- 1. For all other problems P2 in NP, $P2 \propto P$ -NPC.
- 2. This implies that to show a new problem P-NEW is NPC:
 - \rightarrow We have to show that P-NEW is in NP (solution can be verified in P time)
 - → We have to show that for some other NPC problem such as P-NPC, P-NPC ∝ P-NEW
 - By transitivity, then all other problems in NP are \propto P-NEW Because {All NP} \propto P-NPC \propto P-NEW



Example: NP Complete

- A Hamilton circuit is a path in a graph that visits each node exactly once. Assume we know that the directed Hamilton circuit problem is NP-Complete (it is). Show that the undirected Hamilton circuit problem is also NP-Complete.
- Strategy: $Hamilton_{directed} \propto Hamilton=_{undirected}$

Hamilton Circuit

1. Show that the undirected problem is in NP by verifying solution in polynomial time.

Answer: Given a proposed solution, we can start at any vertex and follow the path, marking each vertex as we go. When we reach the original vertex without having visited any marked vertices, and after having visited every vertex, we are done and can output a YES. O(V) time.





Hamilton Circuit

- Note that all nodes must be visited in sequence 1-2-3 or 3-2-1, since 3 and 1 are always connected, and 2 is always in the middle.
- Thus any hamilton circuit discovered on the undirected graph translates back into the directed graph. We can do the transformation both ways in O(V+E) time, where E is the edges and V are the vertices.

Example: TSP

- Show that the decision version of the Traveling Salesman Problem (is there a Hamilton Circuit with total edge weight cost ≤ k?) is NPC. Assume that we know the Hamilton Circuit problem is NPC.
- Strategy: Hamilton \propto TSP

TSP

- 1. First, show that TSP is in NP. This is easy for the decision version of the problem. Given a proposed solution (a tour and the constant k) we simply add up the cost on all the edges, make sure this is a valid tour, and that the total cost is $\leq k$. If so, the solution is correct.
- 2. Show that Hamilton Circuit is reducible to TSP. To do this, we simply construct a special version of the TSP. We make a weight of 1 for every edge in the graph and set k equal to any number ≥ the total number of nodes. Any answer found by the TSP solution must also therefore be a valid Hamilton Circuit.
- It is very difficult to prove that the general Hamilton Circuit problem is NP Complete.







Clique is NPC

2. Show that 3SAT is polynomial reducible to Clique. To do this, we create a special graph that is designed to mimic the behavior of the variables and clauses in the 3SAT problem.

Let Φ be a formula with k clauses such as:

 $\Phi = (\mathbf{a}_1 \lor \mathbf{b}_1 \lor \mathbf{c}_1) \land (\mathbf{a}_2 \lor \mathbf{b}_2 \lor \mathbf{c}_2) \land \ ... \ (\mathbf{a}_k \lor \mathbf{b}_k \lor \mathbf{c}_k)$

3SAT to Clique

The reduction creates the undirected graph G as follows. The nodes in G are organized into k groups of three nodes each called the triples t₁, t₂, ... t_k. Each triple represents one of the clauses in Φ. Edges are present between all pairs of nodes in G, except for nodes in the same triple, and nodes of opposite labels, e.g. x₁ and ¬x₁. For example, given:





You should be wondering...

- We can show other algorithms to be NP-Complete by showing an existing NPC problem can be polynomially reduced to the new algorithm. But how do we prove the first NPC problem?
- Answer: The first problem proven to be NP-Complete is the circuit satisfiability problem. This is known as Cook's Theorem. Based on Cook's theorem, other theorists were able to prove hundreds of other problems to be NP-complete.















CSAT

• Second, we must show that every language in NP is polynomial-time reducible to CSAT. Our proof is based on the workings of an actual computer (and thus is translatable to the workings of a Turing Machine). This is much harder to prove, and here we only give a sketch of the formal proof.



Configurations

 This mapping from one configuration to another can be accomplished by a combinational circuit (in fact, this is what is done by the computer). Call this combinatorial circuit, M. The execution of a program taking Z+1 steps can then be viewed as the following:



CSAT is NPC

- Let L be any language in NP. By definition, L has a verification algorithm, V(x,y), that runs in polynomial time.
- This means that if the input x is of length n, then there is a constant k such that the runtime of V, T(V), is O(n^k). Similarly, the length of the certificate, y, must also be O(n^k).







Wrapup of CSAT

- In the other direction, suppose that some certificate exists. When we feed this certificate into the circuit, it will produce an output of 1. Thus, if the original problem is solvable, this instance of CSAT is also satisfiable.
- To complete the proof, we must show that the circuit can be constructed in polynomial time i.e. the reduction is polynomial.
 - Since the verification algorithm runs in polynomial time, there are only a polynomial number of configurations.
 - This means we are hooking together some polynomial number of circuits M. M can be constructed in size polynomial to the length of a configuration making the overall construction time polynomial.
- Based on the two properties of CSAT, we conclude that every language in NP reduces to CSAT in polynomial time and since CSAT is in NP, it is NP Complete.